

Claim Amendments

1. (Currently Amend) A sound generation arrangement for a computing system, comprising:
 - a predetermined sound generation arrangement to generate sound according to primary sound instructions implemented in the computing system;
 - a monitor arrangement to monitor for predetermined evidence of occurrence of ancillary sound instructions which differ from said primary sound instructions; and
 - an ancillary waveform library arrangement containing predetermined ancillary waveforms, and wherein
the predetermined sound generation arrangement is adapted to use said predetermined ancillary waveforms to emulate predetermined ancillary sounds responsive to an ancillary sound instruction as monitored by said monitor arrangement.
2. (Original) A sound generation arrangement as claimed in claim 1, wherein said monitor arrangement is a snoop arrangement adapted to snoop a predetermined communication path for occurrence of said ancillary sound instructions.
3. (Original) A sound generation arrangement as claimed in claim 1, wherein said monitor arrangement is a snoop arrangement adapted to snoop a predetermined storage location for occurrence of said ancillary sound instructions.
4. (Original) A sound generation arrangement as claimed in claim 1, wherein said monitor arrangement is arranged to monitor a state of an ancillary sound

generation arrangement as indication of occurrence of said ancillary sound instructions.

5. (Original) A sound generation arrangement as claimed in claim 1, wherein said monitor arrangement is responsive to an interrupt as evidence of occurrence of said ancillary sound instructions.

6. (Original) A sound generation arrangement as claimed in claim 1, wherein said sound generation arrangement is adapted to operation in accordance with an Audio Codec (AC) >97 specification, and wherein said ancillary sound instruction is a pre-AC >97 sound instruction.

7. (Original) A sound generation arrangement as claimed in claim 1, wherein said sound generation arrangement is provided at least partially as part of a chipset.

8. (Original) A sound generation arrangement as claimed in claim 1, wherein said primary sound instructions are instructions in accordance with a contemporary sound specification, whereas said ancillary sound instructions are instructions with a legacy sound specification.

9-15. (Canceled).

16. (Currently Amended) A system comprising:
a predetermined sound generation arrangement to generate sound according to primary sound instructions implemented in the computing system;
a monitor arrangement to monitor for predetermined evidence of occurrence of ancillary sound instructions which differ from said primary sound instructions; and

an ancillary waveform library arrangement containing predetermined ancillary waveforms, and wherein

the predetermined sound generation arrangement is adapted to use said predetermined ancillary waveforms to emulate predetermined ancillary sounds responsive to an ancillary sound instruction as monitored by said monitor arrangement.

17. (Original) A system as claimed in claim 16, wherein said monitor arrangement is a snoop arrangement adapted to snoop a predetermined communication path for occurrence of said ancillary sound instructions.

18. (Original) A system as claimed in claim 16, wherein said monitor arrangement is a snoop arrangement adapted to snoop a predetermined storage location for occurrence of said ancillary sound instructions.

19. (Original) A system as claimed in claim 16, wherein said monitor arrangement is arranged to monitor a state of an ancillary system as indication of occurrence of said ancillary sound instructions.

20. (Original) A system as claimed in claim 16, wherein said monitor arrangement is responsive to an interrupt as evidence of occurrence of said ancillary sound instructions.

21. (Original) A system as claimed in claim 16, wherein said sound generation arrangement is adapted to operation in accordance with an Audio Codec (AC) >97 specification, and wherein said ancillary sound instruction is a pre-AC >97 sound instruction.

22. (Original) A system a claimed in claim 16, wherein said sound generation arrangement is provided at least partially as part of a chipset.

23. (Original) A system as claimed in claim 16, wherein said primary sound instructions are instructions in accordance with a contemporary sound specification, whereas said ancillary sound instructions are instructions with a legacy sound specification.

24-32. (Canceled).

33. (Original) A sound generation arrangement for a computing system, comprising:

a monitor arrangement to monitor for indication of occurrence of a predetermined legacy sound instruction; and
a legacy waveform library arrangement containing predetermined legacy waveforms, and adapted to use said predetermined legacy waveforms to emulate predetermined legacy sounds responsive to a legacy sound instruction indicated by said monitor arrangement.

34. (New) A sound generation arrangement as claimed in claim 33, wherein said monitor arrangement is a snoop arrangement adapted to snoop a predetermined communication path for occurrence of said legacy sound instructions.

35. (New) A sound generation arrangement as claimed in claim 33, wherein said monitor is a snoop arrangement adapted to snoop a predetermined storage location for occurrence of said legacy sound instructions.

36. (New) A sound generation arrangement as claimed in claim 33, wherein

said monitor arrangement is arranged to monitor a state of an legacy sound generation arrangement as indication of occurrence of said legacy sound instructions.

37. (New) A sound generation arrangement as claimed in claim 33, wherein said monitor arrangement is responsive to an interrupt as evidence of occurrence of said legacy sound instructions.

38. (New) A sound generation arrangement as claimed in claim 33, wherein said legacy sound instruction is a pre-AC'97 sound instruction.

39. (New) An apparatus, comprising:
a monitor to detect a timer instruction associated with a timer generated tone;
and
a controller to emulate the timer generated tone in response to the monitor detecting the timer instruction.

40 (New) An apparatus of claim 39, wherein the monitor snoops a communication path for the timer instruction.

41 (New) An apparatus of claim 39, wherein the monitor snoops a storage location for the timer instruction.

42. (New) An apparatus of claim 39, wherein the monitor detects the timer instruction based upon an interrupt associated with the timer instruction.

43. (New) An apparatus of claim 39, wherein the controller retrieves a waveform associated with the timer instruction from a waveform library to emulate the timer generated tone.

44. (New) A method, comprising:

detecting one or more instructions associated with instructing a timer to generate a tone; and

emulating a timer generated tone with a sound controller in response to detecting the one or more instructions.

45. (New) A method of claim 44, wherein detecting comprises monitoring a communication path for the one or more instructions.

46. (New) A method of claim 44, wherein detecting comprises monitoring a storage location for the one or more instructions.

47. (New) A method of claim 44, wherein detecting comprises detecting an interrupt associated with the one or more instructions.

48. (New) A method of claim 44, wherein emulating comprises emulating the timer generated tone based upon a waveform of a waveform library.